



## SWITCH MODE POWER SUPPLY CONTROLLER

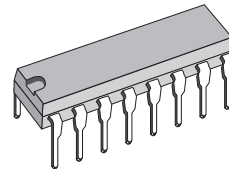
- Positive and Negative Current up to 1.2A and -2A
- Low Start-Up Current
- Direct Drive of the Power Transistor
- Two Levels Transistor Current Limitation
- Double Pulse Suppression
- Soft-Starting
- Under and Overvoltage Lock-out
- Automatic Stand-By Mode Recognition
- Large Power Range Capability in Stand-By (Burst Mode)
- Internal PWM Signal Generator

### DESCRIPTION

The TEA2260/61 is a monolithic integrated circuit for the use in primary part of an off-line switching mode power supply.

All functions required for SMPS control under normal operating, transient or abnormal conditions are provided.

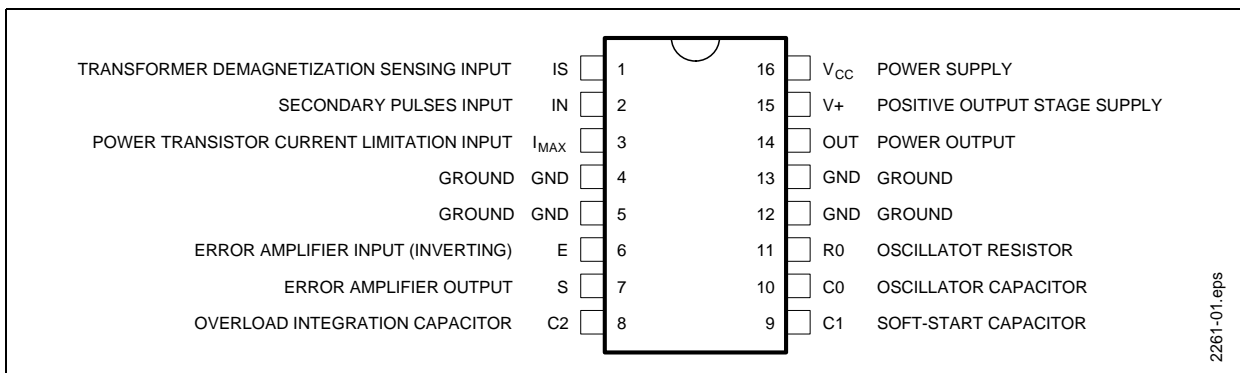
The capability of working according to the "master-slave" concept, or according to the "primary regulation" mode makes the TEA2260/61 very flexible and easy to use. This is particularly true for TV receivers where the IC provides an attractive and low cost solution (no need of stand-by auxiliary power supply). See application note AN376/0490 for detailed information.



**BATWING DIP16  
(Plastic Package)**

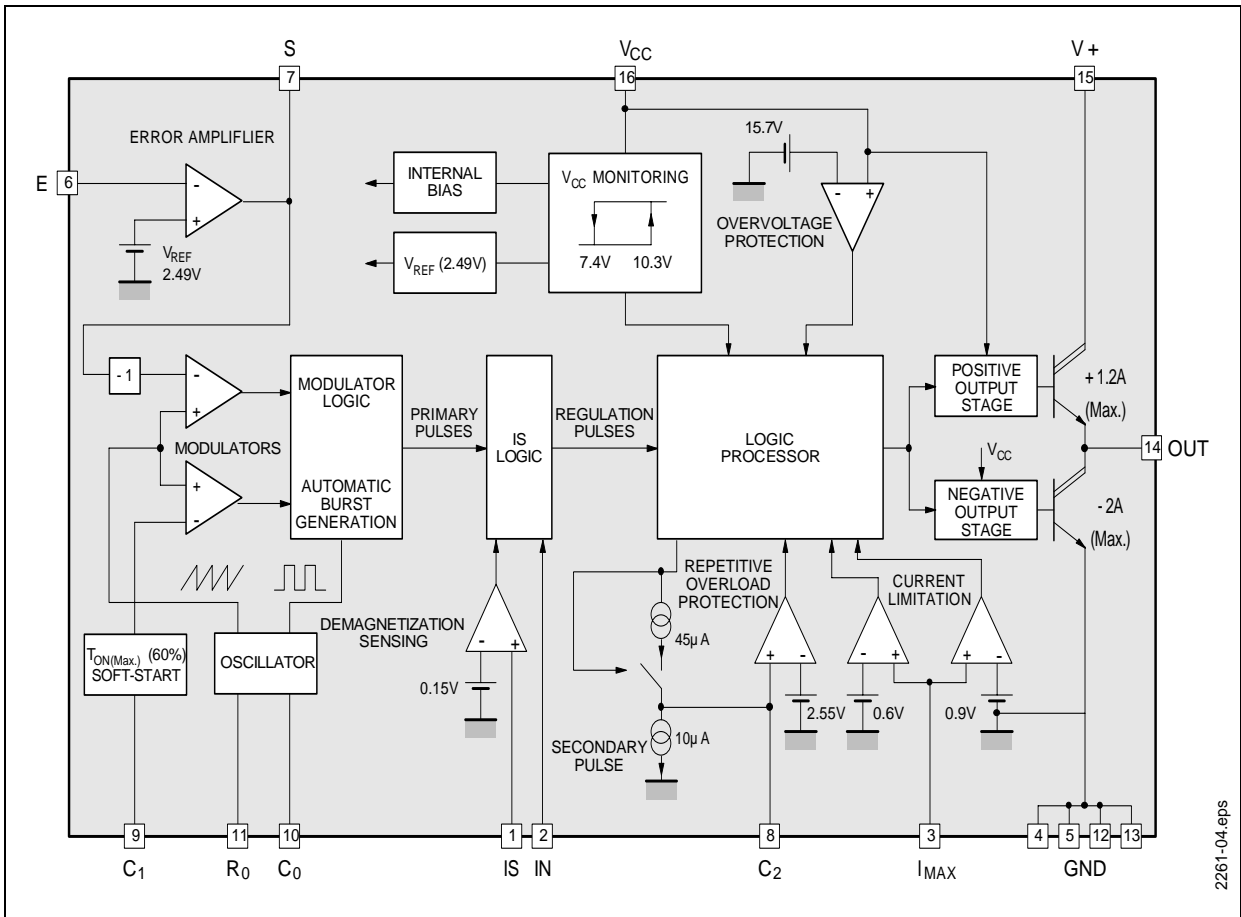
**ORDER CODES:  
TEA2261/0**

**Figure 1. Pin Connections**



TEA2261/0

Figure 2. Block Diagram



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SIMPLIFIED APPLICATION DIAGRAMS

Figure 3. Master-Slave Concept

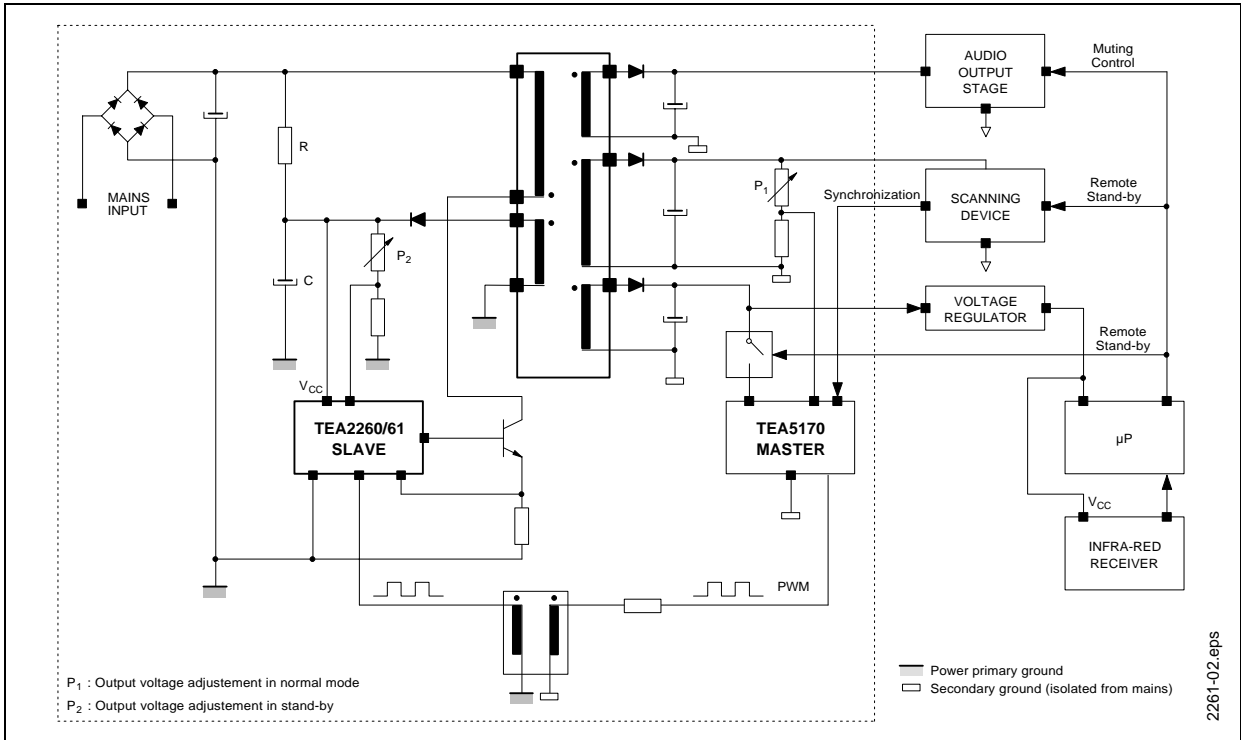
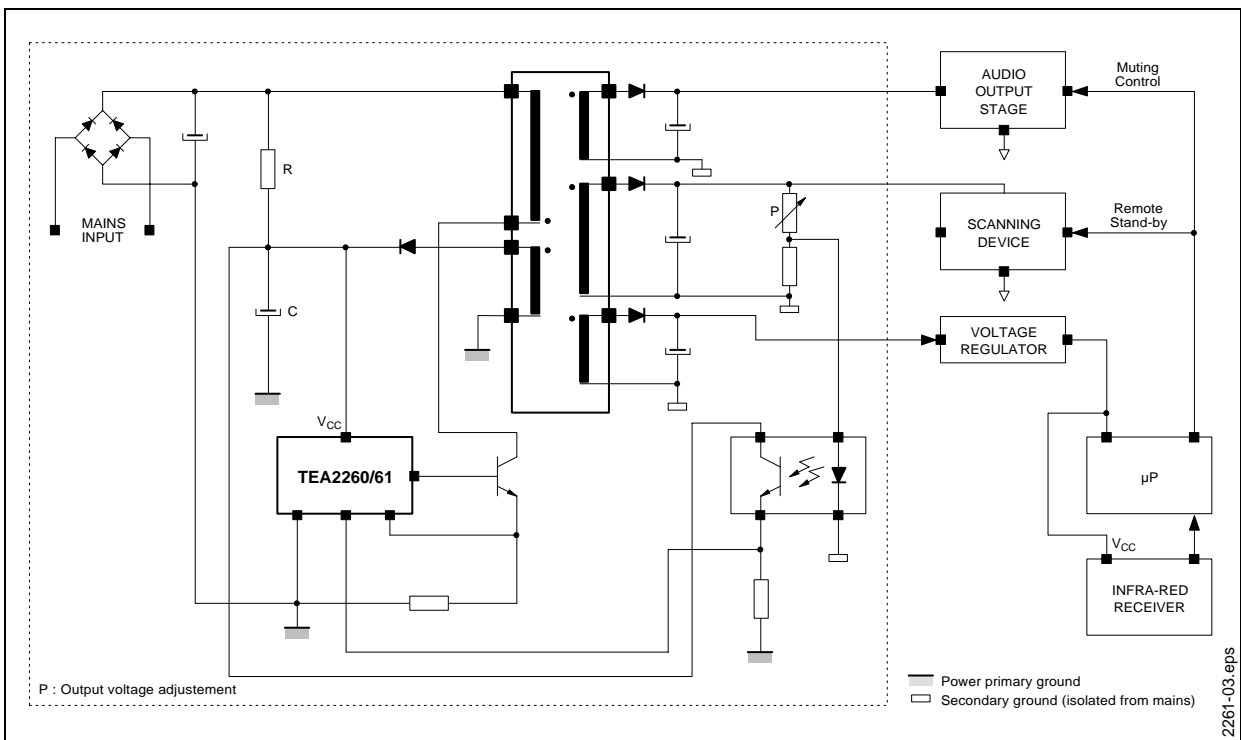


Figure 4. Secondary Regulation (with optocoupler)



## TEA2261/0

### ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V <sub>CC</sub>	Power Supply V16-V4, 5, 12, 13	20	V
V+	Output Stage Power Supply V15-V4, 5, 12, 13	20	V
I <sub>OUT+</sub>	Positive Output Current (source current)	1.5	A
I <sub>OUT-</sub>	Negative Output Current (sink current)	2.5	A
T <sub>j</sub>	Operating Junction Temperature	150	°C
T <sub>stg</sub>	Storage Temperature Range	-40, +150	°C

### THERMAL DATA

Symbol	Parameter	Value	Unit
R <sub>th(j-c)</sub>	Junction-case Thermal Resistance	11	°C/W
R <sub>th(j-a)</sub>	Junction-ambient Thermal Resistance	45	°C/W

**RECOMMENDED OPERATING CONDITIONS**

Symbol	Parameter	Min.	Typ.	Max.	Unit
$V_{CC}$	Power Supply	$V_{CC\ stop}$	12	$V_{CC\ max}$	V
$I_{OUT+}$	Positive Output Current (source current)			1.2	A
$I_{OUT-}$	Negative Output Current (sink current)			2.0	A
$I_{OUT+}$	Average Positive Output Current			0.6	A
$I_{OUT-}$	Average Negative Output Current			0.6	A
$F_{oper}$	Operating Frequency	10		100	kHz
$V_{IN}$	Input Pulses Amplitude (Pin 2)	1.5	2.5	4.5	V
$R_{OSC}$	Oscillator Resistor Range	20		150	k $\Omega$
$C_{OSC}$	Oscillator Capacitor Range	0.47		4.7	nF
C1	Soft-starting Capacitor Range	0.047	1		$\mu$ F
C2	Overload Integration Capacitor	0.047	1		$\mu$ F
C2/C1	Ratio C2/C1 (C2 must be $\leq$ C1)	1			
$T_{amb}$	Operating Ambient Temperature	-20		70	$^{\circ}$ C

**ELECTRICAL CHARACTERISTICS** ( $T_{amb} = 25^{\circ}$ C,  $V_{CC} = 12$ V, unless otherwise specified)

Symbol	Parameter	Min.	Typ.	Max.	Unit
<b>POWER SUPPLY</b>					
$V_{CC(start)}$	Starting Voltage ( $V_{CC}$ increasing)	9.3	10.3	11.3	V
$V_{CC(stop)}$	Stopping Voltage ( $V_{CC}$ decreasing)	6.4	7.4	8.4	V
Hyst $V_{CC}$	Hysteresis ( $V_{CC(start)} - V_{CC(Stop)}$ )	2.4	2.9		V
$I_{CC(start)}$	Starting Current ( $V_{CC} = 9$ V)		0.7	1.4	mA
$I_{CC}$	Supply Current ( $V_{CC} = 12$ V)		7.5	15	mA
$V_{CC(max)}$	Overvoltage Threshold on $V_{CC}$	15	15.7		V
$I_{CC(over)}$	Supply Current after Overvoltage Detection ( $V_{CC} = 17$ V)	26	35	42	mA
<b>OSCILLATOR / PWM SECTION</b>					
$\frac{\Delta F}{F}$	Accuracy ( $R_{OSC} = 68$ k $\Omega$ , $C_{OSC} = 1$ nF)		10		%
$t_{ON\ max}$	Maximum Duty Cycle in Primary Regulation Mode	50	60	70	%
<b>ERROR AMPLIFIER SECTION</b>					
$A_{VO}$	Open Loop Gain		75		dB
$F_{ug}$	Unity Gain Frequency		550		kHz
$I_{SC}$	Short Circuit Output Current (Pin 7 connected to ground)		2		mA
$I_{BE}$	E Input Bias Current (Pin 6)		0.08		$\mu$ A
$V_{REF}$	Internal Voltage Reference (connected to error amplifier input and not directly accessible)	2.34	2.49	2.64	V
<b>INPUT SECTION</b>					
$V_{IN}$	IN Input Threshold (Pin 2)	0.6	0.85	1.2	V
$V_{IS}$	IS Input Threshold (Pin 1)		0.15		V
$I_{BIN}$	IN Input Bias Current		0.3		$\mu$ A
$I_{BIS}$	IS Input Bias Current		0.4		$\mu$ A
<b>CURRENT LIMITATION SECTION</b>					
$V_{IM1}$	First Current Limitation Threshold	558	600	642	mV
$V_{IM2}$	Second Current Limitation Threshold	837	900	963	mV
$DV_{IM}$	Thresholds Difference $V_{IM2} - V_{IM1}$		300		mV
$V_{C2}$	Lock-out Threshold on Pin C2	2.25	2.55	2.85	V
$I_{DC2}$	Capacitor C2 Discharge Current		10		$\mu$ A
$I_{CC2}$	Capacitor C2 Charge Current		45		$\mu$ A
$I_{BI(max)}$	Maximum Input Bias Current (Pin 3)		0.2		$\mu$ A

## TEA2261/0

### GENERAL DESCRIPTION

The TEA2260/61 is an off-line switch mode power supply controller. The synchronization function and the specific operation in stand-by mode make it well adapted to video applications such as TV sets, VCRs, monitors, etc..

The TEA2260/61 can be used in two types of architectures:

- Master/Slave architecture. In this case, the TEA2260/61 drives the power transistor according to the pulse width modulated signals generated by the secondary located master circuit. A pulse transformer provides the feedback (see Figure 1).
- Conventional architecture with linear feedback signal (feedback sources: optocoupler or transformer winding) (see Figure 2).

Using the TEA2260/61, the stand-by auxiliary power supply, often realized with a small but costly 50Hz transformer, is no longer necessary. The burst mode operation of the TEA2260/61 makes possible the control of very low output power (down to less than 1W) with the main power transformer.

When used in a master/slave architecture, the TEA2260/61 and also the power transistor turn-off can be easily synchronized with the line transformer. The switching noise cannot disturb the picture in this case.

As an S.M.P.S. controller, the TEA2260/61 features the following functions:

- Power supply start-up (with soft-start)
- PWM generator
- Direct power transistor drive (+1.2A, -2.0A)
- Safety functions: pulse by pulse current limitation, output power limitation, over and under voltage lock-out.

### S.M.P.S. OPERATING DESCRIPTION

#### Starting Mode - Stand By Mode

Power for circuit supply is taken from the mains through a high value resistor before starting. As long as  $V_{CC}$  of the TEA2260/61 is below  $V_{CC}$  start, the quiescent current is very low (typically 0.7mA) and the electrolytic capacitor across  $V_{CC}$  is linearly charged. When  $V_{CC}$  reaches  $V_{CC}$  start (typically 10.3V), the circuit starts, generating output pulses with a soft-starting. Then the SMPS goes into the stand-by mode and the output voltage is a percentage of the nominal output voltage (e.g. 80%).

To do this, the TEA2260/61 contains all the functions required for primary mode regulation: a fixed frequency oscillator, a voltage reference, an error amplifier and a pulse width modulator (PWM).

For transmission of low power with a good efficiency in stand-by, an automatic burst generation system is used, in order to avoid audible noise.

#### Normal Mode (Secondary Regulation)

The normal operating of the TV set is obtained by sending to the TEA2260/61 regulation pulses generated by a regulator located in the secondary side of the power supply.

This architecture uses the "Master/Slave Concept", advantages of which are now well-known especially the very high efficiency in Stand-by mode, and the accurate regulation in Normal mode.

Stand-by mode or normal mode are obtained by supplying or not the secondary regulator. This can be ordonned for example by a microprocessor in relation with the remote control unit.

Regulation pulses are applied to the TEA2260/61 through a small pulse-transformer to the IN input (Pin 2). This input is sensitive to positive square pulses. The typical threshold of this input is 0.85V. The frequency of pulses coming from the secondary regulator can be lower or higher than the frequency of the starting oscillator.

The TEA2260/61 has no soft-starting system when it receives pulses from the secondary. The soft-start must be located in the secondary regulator.

Due to the principle of the primary regulation, pulses generated by the starting system automatically disappear when the voltage delivered by the SMPS increases.

#### Stand-by Mode - Normal Mode Transition

During the transition there are simultaneously pulses coming from the primary and secondary regulators.

These signals are not synchronized and some care has to be taken to ensure the safety of the switching power transistor.

A very sure and simple way consist in checking the transformer demagnetization state.

- A primary pulse is taken in account only if the transformer is demagnetized after a conduction of the power transistor required by the secondary regulator.
- A secondary pulse is taken in account only if the transformer is demagnetized after a conduction of the power transistor required by the primary regulator.

With this arrangement the switching safety area of the power transistor is respected and there is no risk of transformer magnetization.

The magnetization state of the transformer is checked by sensing the voltage across a winding of the transformer (generally the same which sup-

plies the TEA2261). This is made by connecting a resistor between this winding and the demagnetization sensing input of the circuit (Pin 1).

### SECURITY FUNCTIONS OF THE TEA2260 (see flowchart below)

**Undervoltage Detection.** This protection works in association with the starting device “ $V_{CC}$  switch” (see paragraph Starting-mode - stand-by mode). If  $V_{CC}$  is lower than  $V_{CCstop}$  (typically 7.4V) output pulses are inhibited, in order to avoid wrong operation of the power supply or bad power transistor drive.

**Overvoltage Detection.** If  $V_{CC}$  exceeds  $V_{CCmax}$  (typically 15.7V) output pulses are inhibited. Restarting of the power supply is obtained by reducing  $V_{CC}$  below  $V_{CCstop}$ .

**Current Limitation of the Power Transistor.** The current is measured by a shunt resistor. A double threshold system is used:

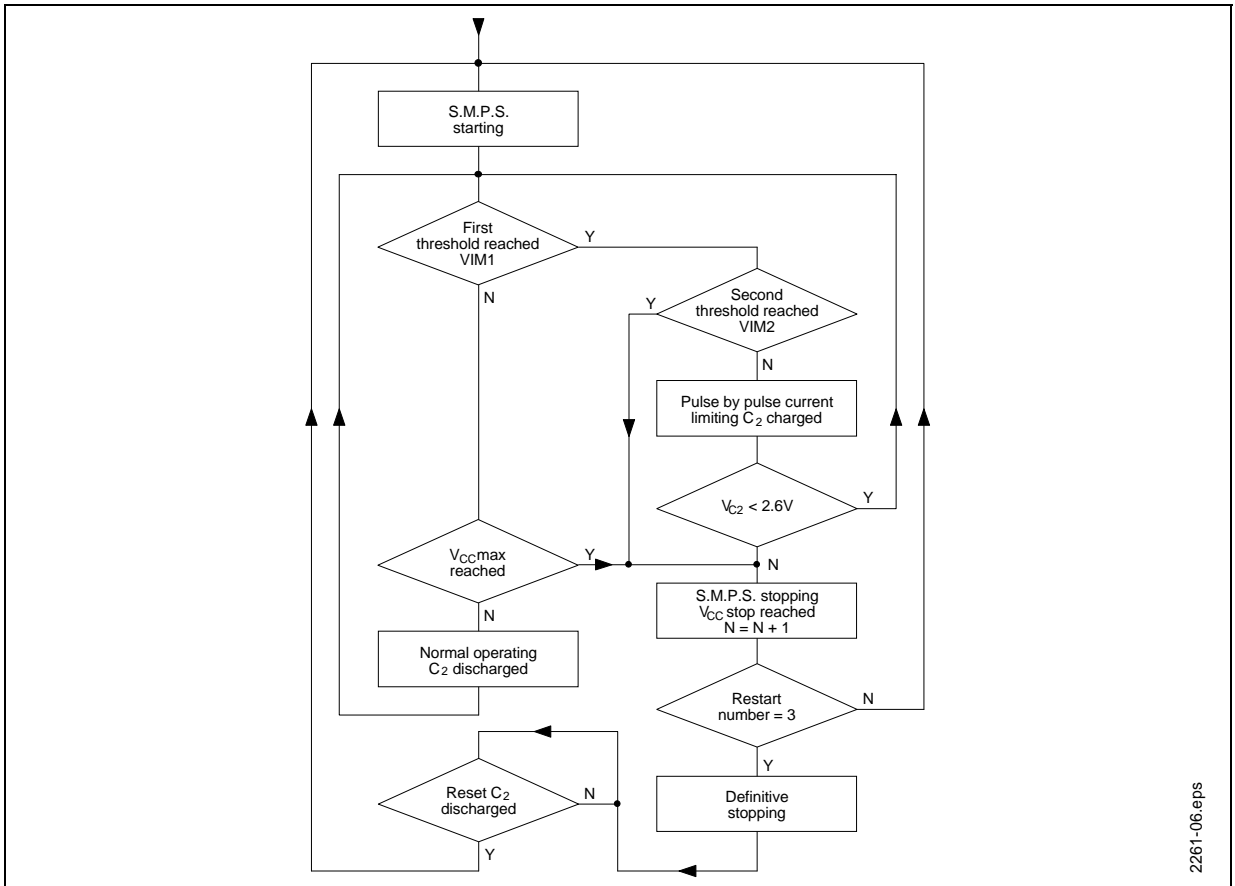
- When the first threshold ( $V_{IM1}$ ) is reached, the conduction of the power transistor is stopped until the end of the period: a new conduction signal is needed to obtain conduction again.
- Furthermore as long as the first threshold is reached (it means during several periods), an external capacitor  $C_2$  is charged. When the voltage

across the capacitor reaches  $V_{C2}$  (typically 2.55V) the output is inhibited. This is called the “repetitive overload protection”. If the overload disappears before  $V_{C2}$  is reached,  $C_2$  is discharged, so transient overloads are tolerated.

- Second current limitation threshold ( $V_{IM2}$ ). When this threshold is reached the output of the circuit is immediately inhibited. This protection is helpful in case of hard overload for example to avoid the magnetization of the transformer.

**Restart of the Power Supply.** After stopping due to  $V_{C2}$ ,  $V_{IM2}$ ,  $V_{CCMax}$  or  $V_{CCstop}$  triggering, restart of the power supply can be obtained by the normal operating of the “ $V_{CC}$  switch” but thanks to an integrated counter, if normal restart cannot be obtained after three trials, the circuit is definitively stopped. In this case it is necessary to reduce  $V_{CC}$  below approximately 5V to reset the circuit. From a practical point of view, it means that the power supply has to be temporarily disconnected from any power source to get the restart.

Figure 5. Security Flowchart (TEA2260)



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**SECURITY FUNCTIONS OF THE TEA2261 (see flowchart below)**

**Undervoltage Detection.** This protection works in association with the starting device “ $V_{CC}$  switch” (see paragraph Starting-mode - stand-by mode). If  $V_{CC}$  is lower than  $V_{CCstop}$  (typically 7.4V) output pulses are inhibited, in order to avoid wrong operation of the power supply or bad power transistor drive.

**Overvoltage Detection.** If  $V_{CC}$  exceeds  $V_{CCmax}$  (typically 15.7V) output pulses are inhibited and the external capacitor  $C_2$  is charged as long as  $V_{CC}$  is higher than  $V_{CCstop}$ . Restarting of the power supply is obtained by reducing  $V_{CC}$  below  $V_{CCstop}$  except if the voltage across  $C_2$  reaches  $V_{C2}$  (typically 2.55V) (refer to “Restart of the power supply” paragraph). In this last case, the circuit is definitively stopped.

**Current Limitation of the Power Transistor.** The current is measured by a shunt resistor. A double threshold system is used:

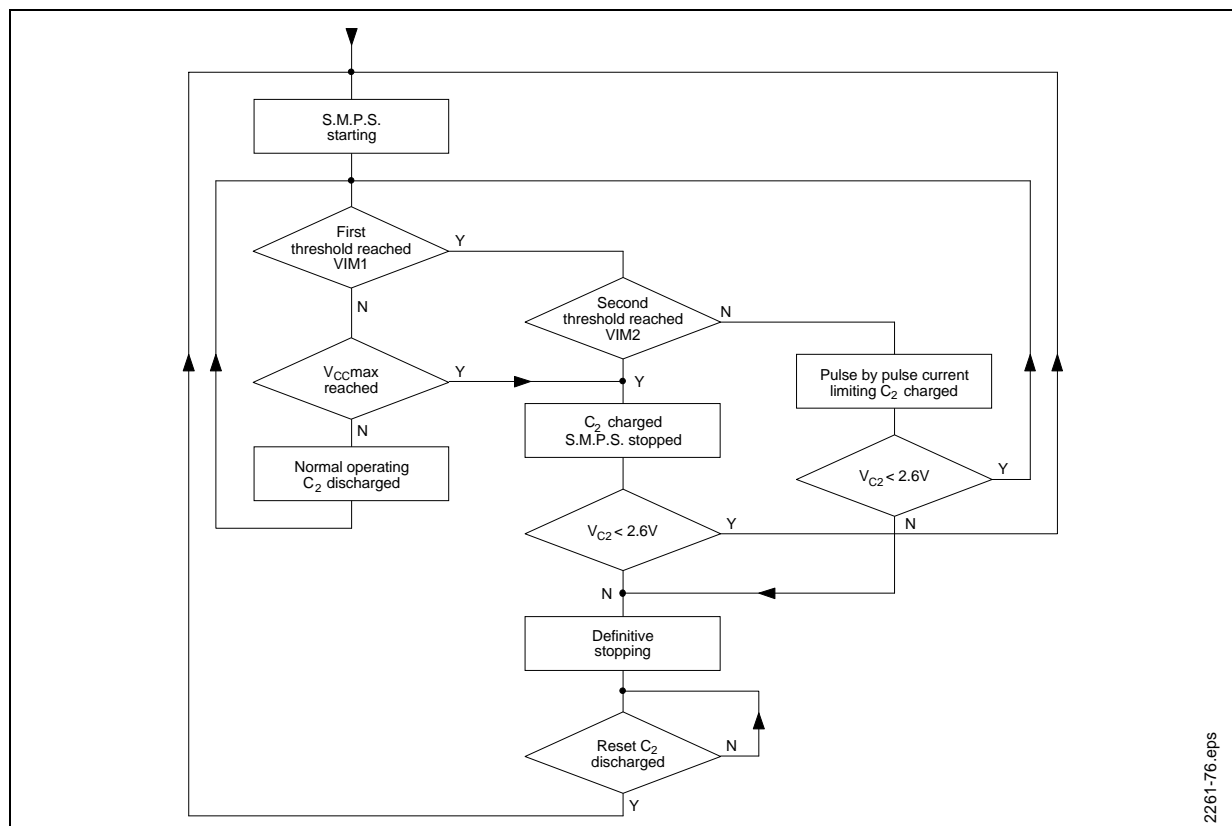
- When the first threshold ( $V_{IM1}$ ) is reached, the conduction of the power transistor is stopped until the end of the period: a new conduction signal is needed to obtain conduction again.

- Furthermore as long as the first threshold is reached (it means during several periods), an external capacitor  $C_2$  is charged. When the voltage across the capacitor reaches  $V_{C2}$  (typically 2.55V) the output is inhibited. This is called the “repetitive overload protection”. If the overload disappears before  $V_{C2}$  is reached,  $C_2$  is discharged, so transient overloads are tolerated.

- Second current limitation threshold ( $V_{IM2}$ ). When this threshold is reached the output of the circuit is immediately inhibited. This protection is helpful in case of hard overload for example to avoid the magnetization of the transformer.

**Restart of the Power Supply.** After stopping due to  $V_{IM2}$ ,  $V_{CCMax}$  or  $V_{CCstop}$  triggering, restart of the power supply can be obtained by the normal operating of the “ $V_{CC}$  switch”  $V_{CC}$  switch sequency from  $V_{CCstop}$  to  $V_{CCstart}$ . After stopping due to  $V_{C2}$  threshold reaching, the circuit is definitively stopped. In this case it is necessary to reduce  $V_{CC}$  below approximately 5V to reset the circuit. From a practical point of view, it means that the power supply has to be temporarily disconnected from any power source to get the restart.

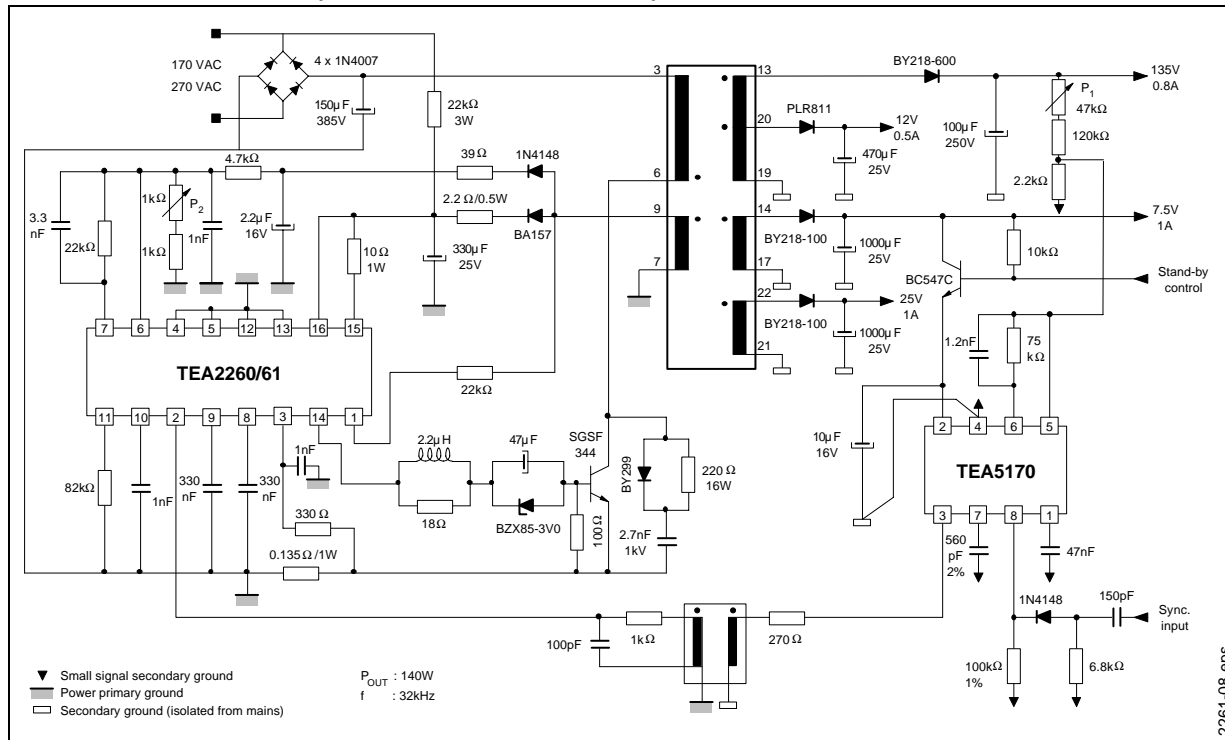
**Figure 6. Security Flowchart (TEA2260)**



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# TEA2261/0

## TYPICAL APPLICATION (Master/Slave Architecture)



### TV - SET SMPS (with TEA5170)

Input voltage range	170VAC - 270VAC
Input DC voltage range	210VDC - 370VDC
Output power in normal mode	$25W < P_O < 140W$
Output power in stand by mode	$2W < P_O < 45W$
Operating frequency	32 kHz
Efficiency at full load	> 80%
Efficiency in stand by mode	> 50%
Short circuit protected	
Open load protected	
Long duration overload protected	
Complete shutdown after repetitive default detection	

### Load Regulation (VDC = 310V)

Output 135V ( $\pm 0.18\%$ )	-> ( $I_{135} : 0.01A$ to $0.8A$ ; $I_{25} = 1A$ )
Output 25V ( $\pm 2\%$ )	-> ( $I_{135} : 0.8A$ ; $I_{25} : 0.5A$ --> $1A$ )

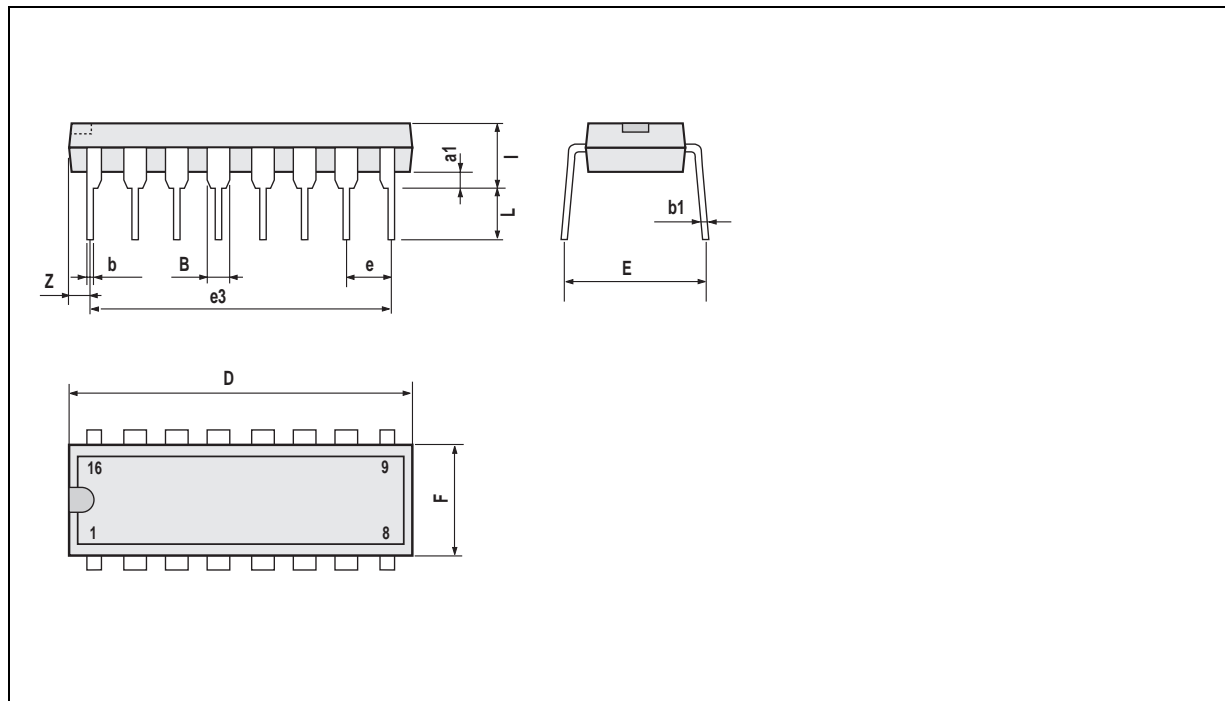
### Line Regulation

Output 135V ( $\pm 0.13\%$ )	-> ( $210V < V_{DC} < 370V$ )
Output 25V ( $\pm 0.17\%$ )	-> ( $I_{135} : 0.8A$ ; $I_{25} : 1A$ )

## PACKAGE MECHANICAL DATA

## 16-PINS - PLASTIC DIP

Figure 7. 16-Pin Package



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